



**IEEE**



**May 23 - 26, 2022**

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## **IEEE MICROELECTRONICS DESIGN & TEST SYMPOSIUM**

### **Industry Trends as Globalized Supply Chains Restructure**

Joe Fitzgerald, Principal, Deloitte Consulting LLP will share the Deloitte Semiconductor and Technology practices outlook on emerging industry trends resulting from COVID/Post-COVID, Geopolitical and Technological pressures. Themes explored will be the industry's manufacturing resiliency, concentration, and capacity in light of shortages, potential impact of the United States CHIPS Act, implications of significant industry manufacturing commitments, shifts in Engineering tools integrating Design to Manufacturing as well as movement of Design and Manufacturing computing loads to the cloud.

#### **Joseph Fitzgerald**



Joseph Fitzgerald is a principal in Deloitte's Supply Chain practice focused on high-tech supply chain strategy, product development, and manufacturing. Joseph leads Deloitte's supply chain innovation offering including product design analytics, advanced manufacturing, and robotics. He has experience developing and implementing supply chain solutions from the factory floor to planning, procurement, product development, and service. He has also helped clients improve engineering efficiency and service effectiveness in the high-tech, telecom, and medical device industries. Joseph's

consulting specialty is derived from 10 years of management and engineering experience in the semiconductor equipment industry, electronic materials science research, and high tech manufacturing.

## **Garrett Wyatt**

Business Sales Executive for Siemens Digital Industries Software

### **Biography:**

Garrett Wyatt is a New Business Sales Executive for Siemens Digital Industries Software who works with industry leading semiconductor companies to select the best tools and design methodologies. Garrett has deep industry knowledge after working in Intel's Advanced Design Technology and Solutions group as a research and design packaging engineer. During that time Garrett was responsible for designing and delivering thermal mechanical test vehicles to support the power-on of market competitive integrated circuits. Garrett holds a BSE in mechanical engineering from Arizona State University and a MSE from Purdue with an emphasis in engineering design.