Call for Papers

The 34th Microelectronics Design & Test Symposium (IEEE MDTS 2025)

May 19 – 21, 2025
Crowne Plaza Albany - The Desmond Hotel in Albany, New York

http://mdts.ieee.org

Key Dates

Full Paper or Extended Summary:
12/15/2024
Notification of Acceptance:
02/16/2025
Final Paper Submission:
04/13/2025

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The IEEE Microelectronics Design & Test Symposium (MDTS) provides a forum for academia and industry to learn about the latest advances in microelectronics and to share research and design work. The Symposium environment fosters academic-industry collaboration with tutorials, a guided panel, invited speakers, original research papers, including hosting student original research communications.

The Program Committee invites researchers and practitioners to submit tutorial, panel, and special session proposals. Proposals must include title, topic abstract, speakers' short bio, and a list of contributing papers. The committee also encourages authors to submit original, unpublished papers on any of the topics of interest. Submissions may be 4 to 6 pages for full papers and two-pages for extended summaries. Accepted full papers presented at the symposium have the option of being published in IEEE *Xplore*®. Selected papers will be invited for possible publication in the Journal of Electronics Testing: Theory and Applications. Details can be found on the **mdts.ieee.org** website.

Papers, tutorials, posters and speakers are solicited in the following topics of interest, but not limited to the following:

- Electronic Testing Technologies
- Design for Test
- Electronic Design Automation (EDA)
- Yield Analysis and Optimization
- Hardware and System Security
- Reliability and Resilience
- Analog/Mixed Signal/RF Circuits and Systems
- Nanoelectronics, Magnetic, Optoelectronics Materials and Devices
- Neuromorphic Engineering
- Quantum Computing
- Power Electronics
- Biomedical, Photonics, and Quantum Electronics
- Sensory Circuits and Systems
- Signal Processing
- Artificial Intelligence (AI) in Electronics
- Beyond-CMOS Nanoelectronics
- Heterogeneous Integration and Packaging
- Wide Bandgap Technologies
- Emerging Technologies and Applications

Best Student Paper Award

To encourage student participation in the microelectronics design and test research community, MDTS has sessions dedicated to student presentations and includes a Best Student Paper Award. We believe that it is important to integrate students into the microelectronics research and development community early in their careers so that they can experience the excitement of direct interaction with their peers.

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To support MDTS-2025, application form is available on mdts.ieee.org; or email chenxinghao@ieee.org

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